

Faculty Profile

Name	Mr.K.S.MURUGESAN			
Department	ELECTRONICS AND COMMUNICATION ENGINEERING			
Qualifications	M.E – VLSI DESIGN			
Area of Specialization	VLSI DESIGN			
Date of Joining (VCET)	09.06.2010			
Date of Birth	14.05.1986			
Experience in (Years)	Teaching	13 Years	Industry	-
Number of Papers Published	National Journals	-	International Journals	8
Number of Papers Presented	National Conferences	11	International Conferences	11
Books Published	2			
Patents	Granted	-	Published	2
Grants Received:	-			
Contact Details	Email:	murugesanks@velalarengg.ac.in , murugesks@gmail.com		
	Mobile	9750365463		

Others:

[Links of Youtube channel, Blogs etc...](#)